

IN THE CLAIMS

Please cancel claims 36, 38, and 39 and amend claim 31 as follows:

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31. (Amended) A memory array strap cell comprising:

a first strap cell body region comprising a well region and operable to be coupled to a first bit cell body region of a bit cell comprising a first transistor including a first active region disposed in the first bit cell body region; and

a first conductive contact coupled to the first strap cell body region;

wherein the strap cell is operable to communicate a first offset voltage potential from a first offset supply line to the first bit cell body region via the first conductive contact and the first strap cell body region; and

wherein the first offset voltage potential is operable to be different from a first supply voltage potential received by the first active region from a first power supply line.

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